

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("6789224").PN.	US-PGPUB; USPAT	OR	OFF	2007/02/12 11:18
L2	14	("4924468" "5365527" "5578935" "5717353" "5909448" "5964894" "6016565" "6092227" "6154074" "6263463" "6360343" "6377065" "6479983").PN. OR ("6789224").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/02/12 11:18
L3	2	((("20050259556") or ("20050271179")).PN.	US-PGPUB; USPAT	OR	OFF	2007/02/12 11:22
L4	15	I1 I2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:22
L5	42473	(semiconductor ic chip "integrated circuit" microprocessor) and (strobe pulse) and phase and (rising falling rise fall) and clock	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:32
L6	14201	(semiconductor ic chip "integrated circuit" microprocessor) and (strobe pulse) and phase and (rising falling rise fall) and clock and (judging judge quality)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:33
L7	3	(semiconductor ic chip "integrated circuit" microprocessor) and (strobe pulse) and phase and (rising falling rise fall) and clock and (judging judge quality) and I4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:48
L8	6	(semiconductor ic chip "integrated circuit" microprocessor) and (strobe pulse) and phase and (rising falling rise fall) and clock and I4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:33

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L9	5	(semiconductor ic chip "integrated circuit" microprocessor) and (strobe pulse) and phase and (rising falling rise fall) and clock and (judging judge quality good bad pass fail\$4) and l4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 13:34
L10	4	("20020003433" "20060036411" "6016566" "6789224").PN. OR ("7126366").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/02/12 13:21
L11	14	("4924468" "5365527" "5578935" "5717353" "5909448" "5964894" "6016565" "6092227" "6154074" "6263463" "6360343" "6377065" "6479983").PN. OR ("6789224").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/02/12 13:27
L12	84	(semiconductor ic chip "integrated circuit" microprocessor) and (strobe pulse) and phase and (rising falling rise fall) and clock and (judging judge quality good bad pass fail\$4) and "714".clas. and "324".clas.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 13:36
L13	2388	(semiconductor ic chip "integrated circuit" microprocessor) and (strobe pulse) and phase and (rising falling rise fall) and clock and (judging judge quality good bad pass fail\$4) and ("714".clas. "324".clas.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 13:35